IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

MOHAMMAD ABDALLAH, ET AL.

Application No.: 09/852,217

Filed: May 8, 2001

For: **Executing Partial-Width Packed Data Instructions**

Art Group: 2183

Examiner: Coleman, Eric

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Reexamination Continuation Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 12-6-04

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Krista Mathieson Dec. 6, 2004

Krista Mathieson ***Date***

US Department of Commerce Patent and Trademark Office	Atty. Docket No.: 42390.P5193C	Application No.: 09/852,217
	Applicant: Abdallah et al.	
Form PTO-1449 (Modified)	Filing Date: May 8, 2001	

US Patent Documents

Examiner's Initials		Date	Document Number	Name	Class	Sub-Class	Filing Date
		07/31/90	4,945,479	Rusterholz et al.			
		11/20/90	4,972,362	Elkind et al.			
		09/10/91	5,047,975	Patti et al.			
		01/14/92	5,081,698	Kohn			
		11/30/92	5,161,247	Murakami et al.			
		02/23/93	5,189,636	Patti et al.			
		07/05/94	5,327,369	Ashkenazi			
		02/14/95	5,390,135	Lee et al.			
		05/07/96	5,515,520	Hatta et al.			
		11/26/96	5,579,253	Lee et al.			
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		04/21/98	5,742,840	Hansen et al.			
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		11/04/03	6,643,765	Hansen et al.			
		04/20/04	6,725,356	Hansen et al.			

Foreign Patent Documents

Examiner's Initials		Date	Document Number	Country	Class	Sub-Class	Translation

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

Examiner	*Date Considered*

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US Department of Commerce Patent and Trademark Office	Atty. Docket No.: 042390.P5193C	Application No.: 09/852,217
	Applicant: Abdallah et al.	
Form PTO-1449 (Modified)	Filing Date: May 8, 2001	

US Patent Documents

Examiner's Initials		Date	Document Number	Name	Class	Sub-Class	Filing Date

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Examiner's Initials		Date	Document Number	Country	Class	Sub-Class	Translation

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

		Santoro, Mark; Design and Clocking of VLSI Multipliers. Technical Report No. CSL -TR-89-397, October 1989. Pages i-xii and 1-118.
		Santoro, Mark, et al.; SPIM: A Pipelined 64X64 bit Interative Multiplier, IEEE Journal of Solid -State Circuits, Vol. 24, No. 2, April 1989. Pages 487-493.
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Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

		Ide, et al.; A 320-MFLOPS CMOS Floating-Point Processing Unit for Superscalar Processors. IEEE Journal of Solid State Circuits, Vol. 28, No. 3, March 1993. Pages 352 - 361.
		Lino, et al.; ISSCC 92 SESSION 6/ MICROPROCESSORS/ TA 6.5: A 289MFLOPS Single-Chip Supercomputer. 1992 IEEE International Solid State Circuits Conference. Pages 112-113.
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		Manferdelli, John L. et al.; Signal Processing Aspects of the S-1 Multiprocessor Project. UCRL-84658. July 28, 1980. Pages 1-8.
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		Undy, Steve, et al.; A Low-Cost Graphics and Multimedia Workstation Chip Set. April 1994, IEEE micro, pages 10 - 22.

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